IN THE SPECIFICATION

Please amend the specification at page 1, lines 4-11, as follows:

Cross-Reference to Related Applications

The present application is related to United States Patent Application Serial Number 10/764,150——, entitled "Method and Apparatus for Reducing Leakage Current in a Read Only Memory Device Using Shortened Precharge Phase," (Attorney Docket No. Dudeck 1-4-32-5), and United States Patent Application Serial Number 10/764,000——, entitled "Method and Apparatus for Reducing Leakage Current in a Read Only Memory Device Using Transistor Bias," (Attorney Docket No. Dudeck 3-6-11-34-7), each filed contemporaneously herewith and incorporated by reference herein.